

**Notice of References Cited**

Application/Control No.

10/784,125

Applicant(s)/Patent Under  
Reexamination  
KOSLOW ET AL.

Examiner

Mary C. Jacob

Art Unit

2123

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*	A	US-6,587,995	07-2003	Duboc et al.	716/4
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	X	Guerra et al, "Cycle and Phase Accurate DSP Modeling and Integration for HW/SW Co-Verification", DAC '99, New Orleans, LA, 1999

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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